

**22883**

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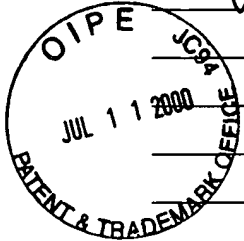
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GROUP 2737-2664

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
m	A	4,866,706	Sept. 12, 1989	Christophersen, et al.	370	85.7
	B	5,920,267	July 6, 1999	Tattersall, et al.	340	825.05
	C	5,465,251	Nov. 7, 1995	Judd, et al.	370	54
	D	5,826,037	Oct. 20, 1998	Stiegler, et al.	395	200.81



## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS	TRANSLATION	
								YES	NO

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

m		PCI Local Bus/ PCT to PCI Bridge Architecture Specification, Revision 1.0, April 5, 1994.
		PCI Local Bus Specification, Production Version, Revision 2.1, June 1, 1995.
		Ivan Tving, Master Thesis, DTH ID-E 579, "Multiprocessor Interconnection Using SCI, August 28, 1994.

EXAMINER

AST PATR

DATE CONSIDERED

11/30/99